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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/789,190	02/27/2004	Ebrahim H. Hargan	303.878US1	6975
21186	7590	08/18/2006		EXAMINER
SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A. P.O. BOX 2938 MINNEAPOLIS, MN 55402			LE, THONG QUOC	
			ART UNIT	PAPER NUMBER
			2827	

DATE MAILED: 08/18/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	10/789,190	HARGAN, EBRAHIM H.
	Examiner	Art Unit
	Thong Q. Le	2827

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 14 June 2006.
 2a) This action is **FINAL**. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-56 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 14-56 is/are allowed.
 6) Claim(s) 1,3,4 and 7 is/are rejected.
 7) Claim(s) 2,3,5,6 and 8-13 is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) Notice of References Cited (PTO-892)
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
 Paper No(s)/Mail Date _____.

4) Interview Summary (PTO-413)
 Paper No(s)/Mail Date. _____.
 5) Notice of Informal Patent Application (PTO-152)
 6) Other: _____.

DETAILED ACTION

1. Amendment filed on 06/14/2006 has been entered.
2. Claims 1-56 are presented for examination.

Response to Arguments

3. Applicant's arguments with respect to claims 1-56 have been considered but are moot in view of the new ground(s) of rejection.

Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 1,3-4,7 are rejected under 35 U.S.C. 102(e) as being anticipated by Fujisawa et al. (Pub. U.S. Patent No. 2003/0198084).

Regarding claim 1, Fujisawa et al. disclose a device (Figure 16) comprising:
a plurality of data lines (Figure 19, DL) ;

a memory array for storing memory data (Figure 10, MC) ;
a conditioning data storage unit (Figure 16, BMRY) for storing conditioning data;
a data selection circuit (Figure 16, SEL) connected to the memory array and the
conditioning data storage unit for selecting data between the memory data and the
conditioning data ([0050]);
a data transceiver circuit (Figure 15, BMRA) connected to the data selection
circuit (Figure 15, SL, SELECT BLOCK) for outputting to the data lines (Figure 10, DL)
the data selected by the data selection circuit ([0047-0048]); and
a strobe transceiver circuit (Figure 15, [0063], [0065]) for providing timing
information of the data outputted at the data lines ([0076]).

Regarding claims 3-4, Fujisawa et al. disclose wherein the conditioning data
storage unit includes multiple cells for storing multiple bits data ([0094]).

Regarding claim 7, Fujisawa et al. disclose a device (Figure 16) comprising:
a plurality of data lines (Figure 19, DL) ;
a memory array for storing memory data (Figure 10, MC) ;
an output data path (Figure 16, i/o) connected to the memory cells;
a conditioning data storage unit (Figure 16, BMRY) for storing conditioning data;
a plurality of multiplexers ([0095]), each of the multiplexers including a first input
node connected to the output data path, a second input node connected to the
conditioning data storage unit, and an multiplexing output node;

a plurality of data transceivers (Figure 15, BMRA) connected to the data selection circuit (Figure 15, SL, SELECT BLOCK) for outputting to the data lines (Figure 10, DL) the data selected by the data selection circuit ([0047-0048]); and a plurality of strobe transceivers (Figure 15, [0063], [0065]) for providing timing information of the data outputted at the data lines ([0076]).

Allowable Subject Matter

6. Claims 2-3, 5-6, 8-13 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 2-3, 4, 8-13 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the claimed limitations. Fujisawa et al. (Pub. U.S. Patent No. 2003/0198084), and others, does not teach the claimed invention having an output enable unit having a driver enable circuit connected to the strobe and data transceiver circuits for simultaneously enabling the strobe and data transceiver circuits at a first time during a memory operation as claims 2-3 disclosed, and wherein the conditioning data storage unit is configured as a read-only storage unit as claims 4 disclosed, and wherein the conditioning data storage unit includes a register connected to the second input node of the multiplexer as claims 8-13 disclosed.

7. Claims 14-56 are allowed.

Claims 14-56 include allowable subject matter since the prior art made of record and considered pertinent to the applicant's disclosure does not teach or suggest the

claimed limitations. Fujisawa et al. (Pub. U.S. Patent No. 2003/0198084), and others, does not teach the claimed invention having a storage node for storing conditioning data as claims 14-18 disclosed, and a controller connected with data bus as claims 1927 disclosed, and a method as claims 28-56 disclosed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thong Q. Le whose telephone number is 571-272-1783. The examiner can normally be reached on 8:00am-5:00pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zarabian Amir can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Thong Q. Le
Primary Examiner
Art Unit 2827

8/14/2006

